IN THE CLAIMS:

Please amend claims 1, 5 and 16 as follows:

1. (Currently Amended) An image display device comprising a display section comprised of a plurality of pixels; and a control section which controls said display section,

wherein said image display device includes a nonvolatile phase-change type memory device having a memory for image display which is comprised of <u>phase-change device elements and TFTs</u>.

- 2. (Original) An image display device according to claim 1, wherein each of said plurality of pixels has a function which retains display data therein.
- 3. (Original) An image display device according to claim 1, wherein said display section is comprised of liquid crystal.
- 4. (Original) An image display device according to claim 1, wherein said display section is comprised of organic light emitting diodes.
- 5. (Currently Amended) An image display device comprising a display section comprised of a plurality of pixels; and a control section which controls said display section,

wherein said image display device includes a nonvolatile phase-change type memory device having a memory for image display which is comprised of <u>phase-change device elements and TFTs</u>, and

said nonvolatile phase-change type memory device is comprised of at least one variable-resistance memory element and at least one TFT.

- 6. (Original) An image display device according to claim 5, wherein said at least one variable-resistance memory element is comprised of a chalcogenide material containing at least one element of Te, Se and S.
- 7. (Original) An image display device according to claim 5, wherein said at least one variable-resistance memory element is fabricated by using a lithographic method, and

is free from variations in resistance value due to registration errors of masks.

- 8. (Original) An image display device according to claim 5, wherein said at least one variable-resistance memory element is covered with a material other than Al such that said at least one variable-resistance memory element is not in direct contact with an Al material.
- 9. (Original) An image display device according to claim 5, wherein said at least one variable-resistance memory element is sandwiched in a direction of a thickness thereof and protected by a plurality of protective films capable of suppressing influences of mobile ions.
- 10. (Original) An image display device according to claim 5, wherein each of said plurality of pixels has a function which retains display data therein.
- 11. (Original) An image display device according to claim 5, wherein said nonvolatile phase-change type memory device is included in said control section, and serves as a frame memory which retains display data for one frame.
- 12. (Original) An image display device according to claim 5, wherein said display section is comprised of liquid crystal.
- 13. (Original) An image display device according to claim 5, wherein said display section is comprised of organic light emitting diodes.
- 14. (Original) An image display device according to claim 5, wherein a resistance of said at least one TFT in a conducting state thereof is in a range of from $10 \text{ k}\Omega$ to $1,000 \text{ k}\Omega$ and a resistance of said at least one variable-resistance memory element in a high-resistance state thereof is $1,000 \text{ k}\Omega$ or more.
- 15. (Original) An image display device according to claim 5, wherein said at least one variable-resistance memory element is disposed in one of a region having an interconnect of circuits and a region which blocks display-producing light.

16. (Currently Amended) An image display device comprising a display section comprised of a plurality of pixels; and a control section which controls said display section,

wherein said image display device includes a nonvolatile phase-change type memory device having a memory for image display which is comprised of <u>phase-change device</u> elements and TFTs, and

said nonvolatile phase-change type memory device is comprised of combinations of memory cells,

each of said memory cells is comprised of one variable-resistance memory element and one TFT, and retains display data represented by one bit or more.

- 17. (Original) An image display device according to claim 16, wherein said at least one variable-resistance memory element is comprised of a chalcogenide material containing at least one element of Te, Se and S.
- 18. (Original) An image display device according to claim 16, wherein said at least one variable-resistance memory element is fabricated by using a lithographic method, and is free from variations in resistance value due to registration errors of masks.
- 19. (Original) An image display device according to claim 16, wherein said at least one variable-resistance memory element is covered with a material other than Al such that said at least one variable-resistance memory element is not in direct contact with an Al material.
- 20. (Original) An image display device according to claim 16, wherein said at least one variable-resistance memory element is sandwiched in a direction of a thickness thereof and protected by a plurality of protective films capable of suppressing influences of mobile ions.
- 21. (Original) An image display device according to claim 16, wherein said nonvolatile phase-change type memory device is included in said control section, and serves as a frame memory which retains display data for one frame.

- 22. (Original) An image display device according to claim 16, wherein said display section is comprised of liquid crystal.
- 23. (Original) An image display device according to claim 16, wherein said display section is comprised of organic light emitting diodes.
- 24. (Original) An image display device according to claim 16, wherein a resistance of said at least one TFT in a conducting state thereof is in a range of from 10 k Ω to 1,000 k Ω and a resistance of said at least one variable-resistance memory element in a high-resistance state thereof is 1,000 k Ω or more.
- 25. (Original) An image display device according to claim 16, wherein said at least one variable-resistance memory element is disposed in one of a region having an interconnect of circuits and a region which blocks display-producing light.